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LISTING OF CLAIMS

The following is a complete list of all claims in this application.

1-26. (Canceled)

27. (Previously Presented) A flat panel display, comprising:

first and second substrates facing each other;

first and second electrodes disposed on the first substrate and insulated from each other by an insulating layer;

a field emission source on the first substrate; and

a grid plate provided with a plurality of apertures corresponding to a pixel area and disposed between the first and second substrates,

wherein the field emission source is formed of carbonaceous materials selected from the group consisting of carbon nanotube (CNT), fullerene (C_{60}), diamond liked carbon (DLC), and graphite.

- 28. (Previously Presented) The flat panel display of claim 27, wherein the field emission source is formed on the second electrodes.
 - 29. (Previously Presented) A flat panel display, comprising:

first and second substrates facing each other;

first and second electrodes disposed on the first substrate and insulated from each other by an insulating layer;

a field emission source on the first substrate; and

a grid plate provided with a plurality of apertures corresponding to a pixel area and disposed between the first and second substrates,

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wherein the field emission source is formed of nanometer size materials selected from the group consisting of carbon nanotube (CNT), and fullerene (C_{60}).

- 30. (Previously Presented) The flat panel display of claim 29, wherein the field emission source is formed on the second electrodes.
 - 31. (Previously Presented) A flat panel display, comprising:

first and second substrates facing each other;

first and second electrodes disposed on the first substrate and insulated from each other by an insulating layer;

a planar field emission source on the first substrate; and a grid plate provided with a plurality of apertures corresponding to a pixel

wherein the grid plate is formed over the first substrate.

32. (Previously Presented) A flat panel display, comprising:

first and second substrates facing each other;

area,

first and second electrodes disposed on the first substrate and insulated from each other by an insulating layer;

a planar field emission source on the first substrate; and

a mesh electrode having a plurality of apertures corresponding to a pixel area and disposed between the first and second substrates.